







DATA SHEET

CURRENT SENSOR - LOW TCR

PT series 5%, 2%, 1% sizes 0402/0603/0805/1206/2010/2512 RoHS compliant & Halogen free





YAGEO Phícomp

1	AGEO	Phicomp		Product specification		
_		Chip Resistor Surface Mount	PT	SERIES	0402/0603/0805/1206/2010/2512	9

<u>SCOPE</u>

This specification describes PT series current sensor - low TCR and high power with lead-free terminations made by thick film process.

APPLICATIONS

- Converters
- Printer equipment
- Server board
- Telecom
- Consumer electronics
- Car electronics

FEATURES

- AEC-Q200 qualified
- Halogen Free Epoxy
- RoHS compliant
- Reduce environmentally
- High component and equipment reliability
- Non-forbidden material used in products/production
- Low resistances applied to current sensing
- Moisture sensitivity level: MSL I

ORDERING INFORMATION - GLOBAL PART NUMBER

Part numbers is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

РΤ	<u>XXXX</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>XX</u>	<u>XXXX</u>	L
	(1)	(2)	(3)	(4)	(5)	(6)	(7)

(I) SIZE

0402 / 0603 / 0805 / 1206 / 2010 / 2512

(2) TOLERANCE

- $F = \pm 1\%$
- $G = \pm 2\%$
- J = ±5%
- "-"= jumper ordering

(3) PACKAGING TYPE

- R = Paper taping reel
- K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

– = Based on spec.
(5) TAPING REEL
07 = 7 inch dia. Reel and standard power
13 = 13 inch dia. Reel and standard power
TW = 7 inch dia, reel and 2 x standard power
3W = 13 includia, reel and 2 x standard power
$7T = 7$ inch dia. reel and $3 \times$ standard power
(6) RESISTANCE VALUE

There are 3~5 digits indicated the resistor value. Letter R is decimal point.

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter L is system default code for order only $^{\left(\text{Note}\right) }$

Resistance rule of number Resistance code rule	global part Example
0RXXX (25 to 910 mΩ)	$0R025 = 25 m\Omega$ $0RI = 100 m\Omega$
(20 00 / 10 112)	$0R91 = 910 \text{ m}\Omega$

ORDERING EXAMPLE

The ordering code of a PT0603 chip resistor, 1/5W, value 0.56 Ω with ±1% tolerance, supplied in 7-inch tape reel is: PT0603FR-7W0R56L.

Νοτε

- I. All our Rchip products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / I2NC can be added (both are on customer request)

Chip Resi	stor Surface Mount	PT	SERIES	0402/0603/0805/1206/2010/2512	ecification <u>3</u> 9
IARKING					
T0402					
Fig. 4	No marking				
Т0603					
	E-24 series / Non-E	series (R	= 250/400/	500 m Ω): 3 digits	
Fig. 2 Value = 220 m Ω	The "R" is used as a	ı decimal ı	point; the	other 2 digits are significant.	
T0805 / PT1206 / PT2010	/ PT2512				
РЭЭП	E-24 series / Non-E	series (R	= 250/400/	500 m Ω): 4 digits	
Fig. 3 Value = 220 mΩ	The "R" is used as a	ı decimal ı	point; the	other 3 digits are significant.	
or further marking info	ormation, please refer to	data sheet	: "Chip res	istors marking".	
	ucted out of a high-grade	i n			
ach end and connected	netal electrodes are adde by a resistive paste. The	d at			
	e is adjusted to give the resistance and laser cutti	_ در		marking layer	

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Fig. 5 Chip resistor outlines

approximately required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the resistance value. Finally, the three external terminations (Cu/Ni/matte tin) are added, as shown in Fig.5.

May. 24, 2018 V.2

protective glass

resistive layer

inner electrode termination (Cu/Ni/matte tin)

inner electrode

ceramic substrate

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Chip Resistor Surface Mount

PT

SERIES



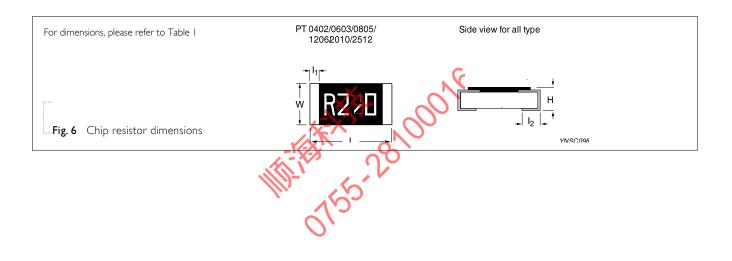
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Product specification

DIMENSIONS

Table I					
ТҮРЕ	L (mm)	W (mm)	H (mm)	l _ı (mm)	l ₂ (mm)
PT0402	1.00 ±0.10	0.50 ±0.05	0.35 ±0.05	0.20 ±0.10	0.25 ±0.10
PT0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
PT0805	2.00 ±0.10	1,25 ±0.10	0.55 ±0.10	0.35 ±0.20	0.35 ±0.20
PT1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.45 ±0.20
PT1206(Note)	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.75 ±0.20	0.45 ±0.20
PT2010	5.00 ±0.10	2.50 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20
PT2512	6.35 ±0.10	3.20 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20

Note: For resistance range: $75m\Omega \leq R < 91m\Omega$



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ELECTRICAL CHARACTERISTICS

Table 2 Type	Power	Operating Temp. range	Max working voltage	Tolerance	Temperature Coe Resistance		Jumper ci	riteria
PT0402	1/16W		Volage		$50m\Omega \leq R < 68m\Omega$ $68m\Omega \leq R < 100m\Omega$	±600ppm/°C ±300ppm/°C	Max. resistance Rated current	10mΩ 3A
	1/8 W				$100m\Omega \leq R < 1\Omega$	±200ppm/°C ⁻		
	I/10W				$50m\Omega$ $50m\Omega < R < 68m\Omega$	0/+400ppm/°C 0/+350ppm/°C	Max. resistance Rated current	8m Ω 5A
PT0603	1/5 W				$68m\Omega \leq R < 100m\Omega$ $100m\Omega \leq R < 1\Omega$	0/+300ppm/°C- ±200ppm/°C		
	1/3 W				50mΩ 50mΩ < R < 68mΩ 68mΩ	0/+400ppm/°C 0/+350ppm/°C 0/+300ppm/°C		
PT0805	1/8 W	-55°C to +155°C	(PxR)^1/2	E24 ± 2%, ± 5% E24/E96 ± 1%	50mΩ 50mΩ <r<68mω< td=""><td>0/+350ppm/°C 0/+300ppm/°C_</td><td>Max. resistance Rated current</td><td>5mΩ 6A</td></r<68mω<>	0/+350ppm/°C 0/+300ppm/°C_	Max. resistance Rated current	5m Ω 6A
110005	1/4 W				$68m\Omega \leq R < 100m\Omega$ $100m\Omega \leq R < 1\Omega$	0/+250ppm/°C ±100ppm/°C		
PT1206	1/4 W			X	$50m\Omega \leq R < 75m\Omega$ $75m\Omega \leq R \leq 100m\Omega$	±350ppm/°C ±100ppm/°C-	Max. resistance Rated current	5m Ω 10A
111200	1/2 W			XX on	$100m\Omega < R < I\Omega$	±75ppm/°C		
PT2010	3/4 W			5 KX31 28				
	IW			10°	100 m Ω	±100 ppm/°C_		
PT2512	1W 2W		(21.2	$100 \text{ m}\Omega < R < 1 \Omega$	±75 ppm/°C -		

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Chip Resistor Surface Mount

PT SERIES





FOOTPRINT AND SOLDERING PROFILES

Recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

 Table 3
 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	PT0402	PT0603	PT0805	PT1206	PT2010	PT2512
Paper taping reel (R)	7" (178 mm)	10,000	5,000	5,000	5,000		
	13" (330 mm)	50,000	20,000	20,000	20,000		
Embossed taping reel (K)	7" (178 mm)					4,000	4,000

NOTE

I. For paper/embossed tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: -55 °C to +155 °C

POWER RATING

Each type rated power at 70 °C: PT0402=1/16W, 1/8W PT0603=1/10W, 1/5W, 1/3W PT0805=1/8W, 1/4W PT1206=1/4W, 1/2W PT2010=3/4W, 1W PT2512=1W, 2W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

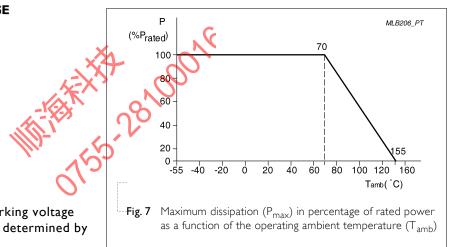
 $V = \sqrt{P \times R}$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$



Chip Ro	esistor Surface Mount	PT SERIES 0402	/0603/0805/1206/2010/2512
fests and requi	REMENTS		
	on, procedure and requirements		
TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Temperature Coefficient of	MIL-STD-202 Method 304	At +25/+125 °C	Refer to table 2
Resistance (T.C.R.)		Formula:	
		T.C.R= $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm)}$	/°C)
		Where t ₁ =+25 °C or specified room ter	nperature
		t ₂ =+125 °C test temperature	
		R ₁ =resistance at reference temp	
		R ₂ =resistance at test temperatur	e in onms
Life/ Endurance	MIL-STD-202 Method 108A IEC 60115-1 4.25.1	I,000 hours at 70±2 °C applied I.5 hours on, 0.5 hour off, still air	
High Temperature Exposure	MIL-STD-202 Method 108A	t,000 hours at maximum operati depending on specification, unpo No direct impingement of forcec Tolerances: 155±3 °C	wered
Moisture Resistance	MIL-STD-202 Method 106	Each temperature / humidity cycl hours (method 106F), 3 cycles / 1 10d with 25 °C / 65 °C 95% R.H 7a & 7b, unpowered	24 hours for
		Parts mounted on test-boards, w condensation on parts	ithout
		Measurement at 24±2 hours afte test conclusion	r
Thermal Shock	MIL-STD-202 Method 107	-55/+125 °C	± (1.0%+0.0005 Ω)
		Number of cycles required is 300 Devices mounted:). Maximum
		transfer time is 20 seconds. Dwe minutes. Air – Air	II time is 15

	chip Resistor Surface Moun	t PT	SERIES	0402/0603/0805/12	206/2010/2512	9
TEST	TEST METHOD	PROCEDUR	E		REQUIREMENTS	s 🗖 🕅 🖓
Short Time Overload	IEC60115-14.13	PT standard p 5 sec at room		nes rated voltage for e	± (1.0%+0.0005 No visible damag	,
		PT high powe at room temp		ed power for 5 sec		
		PT jumper: 2 room temper		current for 5 sec at		
Board Flex/ Bending	IEC 60115-1 4.33	Device moun described, on		est board as nding required	± (1.0%+0.0005 No visible damag	,
		Bending for 0 0			<u></u>	
		Holding time:	minimum 60			
		Ohmic value	checked durir	ng bending		
Solderability						
- Wetting	J-STD-002 test B		50X ns: od B, aging 4	hours at 155 °C	Well tinned (≥95 No visible damaş	,
		dry hi 2 nd step: lead Dipping time:	ree solder ba	ath at 245±3 °C ds		
- Leaching	J-STD-002 test D	Leadfree sold immersion tin		0 seconds	No visible damaş	ge
- Resistance		Condition B, r	no pre-heat c	of samples.	± (0.5%+0.0005	Ω)
Soldering I	Heat	Leadfree sold immersion tin		2, 10±1 seconds	No visible damaş	ge
		Procedure 2 f cleaned with i		ices fluxed and		



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	Chip Resistor Surface Mount			SERIES	0402/0603/0805/1206/2010/2	2512 9
REVISION	<u>HISTORY</u>					
REVISION	DATE	CHANGE NOTIFICATION	DESCRI	PTION		
Version 2	May 24, 2018	-	- Update	e PT0603	7T coding	
Version I	Jul. 02, 2015	-	- Extend	resistor \	value	
Version 0	Aug. 21, 2014	-		603/0805	or current sensor - low TCR PT /1206/2010/2512, 1%, 2%, 5% v	

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